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Linda Shaw	(Depositor's name)
<i>[Signature]</i>	(Signature)
May 14, 2003	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/081,425	02/22/2002	Rajendra Pendse	CPAC 1011-2 US	9980

TITLE OF INVENTION: APPARATUS AND PROCESS FOR PRECISE ENCAPSULATION OF FLIP CHIP INTERCONNECTS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1300	\$300	\$1600	08/08/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
GBYER, SCOTT B	2829	438-127000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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1. Bill Kennedy
2. Haynes Beffel & Wolfeld LLP
3. _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

ChipPAC, Inc.

Fremont, California

Please check the appropriate assignee category or categories (will not be printed on the patent) ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
☒ Publication Fee

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Commissioner for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

(Authorized Signature)

Bill Kennedy, Reg. #33,407

(Date)

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This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, Virginia 22313-1450.

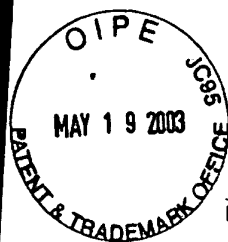
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Mail Stop Issue Fee, Commissioner for Patents, PO Box 1450,
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Linda Shaw

Date

May 14, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Rajendra PENDSE

Application No.: 10/081,425

Filed: February 22, 2002

Title: **Apparatus and process for
precise encapsulation of flip
chip interconnects**

Examiner: Scott B. Geyer

Group Art Unit: 2829

Date: May 14, 2003

SEPARATE LETTER TO OFFICIAL DRAFTSPERSON

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Attn: **Official Draftsperson**

Official Draftsperson:

In connection with the above-referenced U. S. patent application, transmitted herewith are the following papers:

[X] One (1) sheet of formal drawing(s) (Figs 1, 2, 3, 4, and 5).

Authorization is hereby given to deduct from or credit to Deposit Account No. 50-0869 (CPAC 1011-2 US), any additional fees that may be required with this matter.

Respectfully submitted,

Bill Kennedy
Registration No. 33,407

Date: May 14, 2003
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